

<b>Education</b>	<b>PhD in Computer Science (System Software)</b> University of California, Irvine - Advisor: Prof. Michael Franz <b>MSc in Computer Architecture</b> Sharif University of Technology, Tehran/Iran <b>BSc in Computer Engineering</b> Sharif University of Technology, Tehran/Iran	Sep. 2005 - Jul. 2009 GPA 4.0 out of 4.0 Sep. 1998 - Jan. 2001 Graduated with Honors Sep. 1994 - Sep. 1998
<b>Computer Skills</b>	<b>Languages:</b> C++, C, Java, C#, SQL, PHP, Python, HTML, Javascript, XML, VHDL <b>Operating Systems:</b> Linux, Windows, Mac OS X <b>Other techniques and Protocols:</b> Familiar with GCC Internals, Linux kernel and System Calls, Libc Internals, Multithreaded Programming, Superscalar Processor Architecture, MPI, OpenMP, Version Control (SVN, CVS, Perforce)	
<b>Work Experience</b>	<b>Qualcomm Research Center</b> Santa Clara, CA Conducted research on new software technologies for mobile devices to improve power consumption and performance of mobile applications.	Senior System Software Engineer March 2010 - Present
	<b>Yahoo Inc., Search</b> Sunnyvale, CA Improved performance of the searcher nodes' start-up by more than a factor of two using caching and parallelization techniques. Developed an adaptive optimization technique to significantly reduce network bandwidth usage of the search engine back-end. Profiled and analyzed Real Time Search engine to identify bottlenecks and to improve performance. Managed new version of search engine release and deployment.	Senior System Software Engineer August 2009 - February 2010
	<b>VMware Inc.</b> Palo Alto, CA Built custom static analysis checkers using Coverity products. Added functionality to a custom library to detect heap-based buffer overflows. Developed a tool using Python to run static analysis checkers automatically.	Summer Intern June 2008 - September 2008
	<b>Fara Rayaneh Eng. Co.</b> Tehran, Iran Developed a very successful user management and accounting software package with unique features for ISPs and VoIP service providers. Languages, Technologies and protocols used in this project are Visual C++, ASP, Windows services, MS SQL, RADIUS, SNMP.	Director of Software Dept. and Lead Programmer February 2001 - August 2004
	<b>Fara Rayaneh Eng. Co.</b> Tehran, Iran Expanded the internet service capacity of the company to 20 times of its starting point.	Director of the Network Dept. May 1999 - February 2001
	<b>Fara Rayaneh Eng. Co.</b> Tehran, Iran Co-founded the Fara Rayaneh Eng. Co., which later acquired Morva Net and is now one of the largest Iranian Internet Service Providers.	Member of the Board of Directors May 1999 - August 2004
<b>Research Experience</b>	<b>System Software Design</b>	Winter 2010 - Present

Worked in a team of system software designers to create a new framework which enables developers to build power- and performance-efficient applications for mobile devices.

**Software Security and Compilers (PhD Thesis)** Winter 2007 - Spring 2009

Developed a Multi-Variant Execution Environment that runs multiple variants of a single application in lockstep and monitors their behavior at the granularity of system-calls to prevent exploitation of vulnerabilities.

Modified GCC's and also LLVM-GCC's back-end to generate executables that write the stack in the reverse direction. These executables are used in the multi-variant environment to prevent exploitation of stack-based buffer overflow vulnerabilities.

**Energy saving and speedup of Data Cache** Winter 2006 - Spring 2006

Introduced fast address generation and way caching to reduce dynamic and static energy consumption of L1 data cache. Developed simulator and evaluated fast address generation and L0 data cache to reduce memory access latency.

**Optimizing Compiler** Fall 2005

Developed the middle- and back-end for an optimizing compiler using Java.

**Embedded Processors** Fall 2004 - Spring 2005

Customized pipeline gating techniques for embedded processors to reduce energy dissipation caused by branch misprediction. Improved accuracy of small branch predictors used in embedded processors. Proposed a new scoreboard access technique to reduce energy consumption in embedded processors.

**Selected Teaching Experience**

**Teaching Assistant** Compilers and Interpreters  
University of California, Irvine Fall 2007

**Lab. Instructor** Digital Design  
University of Victoria Summer 2005

**Lab. Instructor** Computer Architecture  
University of Victoria Spring 2005

**Awards and Honors**

UCIrvine School of Information and Computer Sciences (ICS) Dean's Fellowship for three years (100,000\$), 2005

University of Victoria Fellowship (\$15000), 2004

Ranked 1st among 10 M.Sc. Fellows, Computer Architecture, Sharif University of Technology, 1998-2001

Ranked 6th among more than 8000 graduates participating in the nationwide M.Sc. program entrance exam, Iran, August 1998

Ranked 115th among more than 400,000 students participating in the nationwide undergraduate entrance exam, Iran, August 1994

**Course Work**

- ▷ Advanced Compiler Construction
- ▷ Advanced Programming Languages
- ▷ Intro. to Parallel and Cluster Computing
- ▷ Advanced Computer Architecture
- ▷ Advanced Computer Networks
- ▷ Operating Systems I & II
- ▷ Language-based Security
- ▷ Real Time Systems
- ▷ Advanced Microprocessor Design
- ▷ Fault Tolerant Systems

**Patent**

"Multi-Variant Parallel Program Execution to Detect Malicious Code Injection," US Application Serial No. 12/075,127 (*pending*), March 2008

**Selected  
Publications**

“Orchestra: Intrusion Detection Using Parallel Execution and Monitoring of Program Variants in User-Space,”

Babak Salamat, Todd Jackson, Andreas Gal, and Michael Franz. *The European Conference in Computer Systems (EuroSys'09)*, March 2009

“Power-Aware Scoreboard Alternatives for Multimedia Processors,”

Amirali Baniyasadi, Babak Salamat, and Kaveh Jokar Deris. *Elsevier Journal of Microprocessors and Microsystems (MICPRO)*, Volume 33, June 2009

“Reverse Stack Execution in a Multi-Variant Execution Environment,”

Babak Salamat, Andreas Gal, and Michael Franz. *The 2008 Workshop on Compiler and Architectural Techniques for Application Reliability and Security (CATARS'08)*, June 2008

“Multi-Variant Program Execution: Using Multi-Core Systems to Defuse Buffer-Overflow Vulnerabilities,”

Babak Salamat, Andreas Gal, Todd Jackson, Karthikeyan Manivannan, Gregor Wagner, and Michael Franz. *International Conference on Complex, Intelligent and Software Intensive Systems (CISIS'08)*, March 2008

“Reverse Stack Execution,”

Babak Salamat, Andreas Gal, Alexander Yermolovich, Karthik Manivannan and Michael Franz. *Technical Report No. 07-07, School of Information and Computer Sciences, University of California, Irvine*, August 2007

“Fast Speculative Address Generation and Way Caching for Reducing L1 Data Cache Energy,”

Dan Nicolaescu, Babak Salamat, Alexander Veidenbaum and Mateo Valero. *Proceedings of 24<sup>th</sup> IEEE International Conference on Computer Design (ICCD'06)*, October 2006

“Area-Aware Optimizations for Resource Constrained Branch Predictors Exploited in Embedded Processors,”

Babak Salamat, Amirali Baniyasadi and Kaveh Jokar Deris. *Proceedings of the International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation (IC-SAMOS)*, July 2006

“Power-Aware Scoreboard for Multimedia Processors,”

Amirali Baniyasadi and Babak Salamat. *7<sup>th</sup> workshop on Media and Streaming Processors (MSP-7) held in conjunction with MICRO-38*, November 2005

“Area-Aware Pipeline Gating for Embedded Processors,”

Babak Salamat and Amirali Baniyasadi. *International Workshop on Power and Timing Modeling, Optimization and Simulation (PATMOS'05)*, September 2005

“Design and implementation of a system for microprocessor laboratory,”

Ghassem Miremadi, Babak Salamat and Amin Firoozshahian. *Proceedings of Sharif University of Tech.*, 1998-1999